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Fig. 1

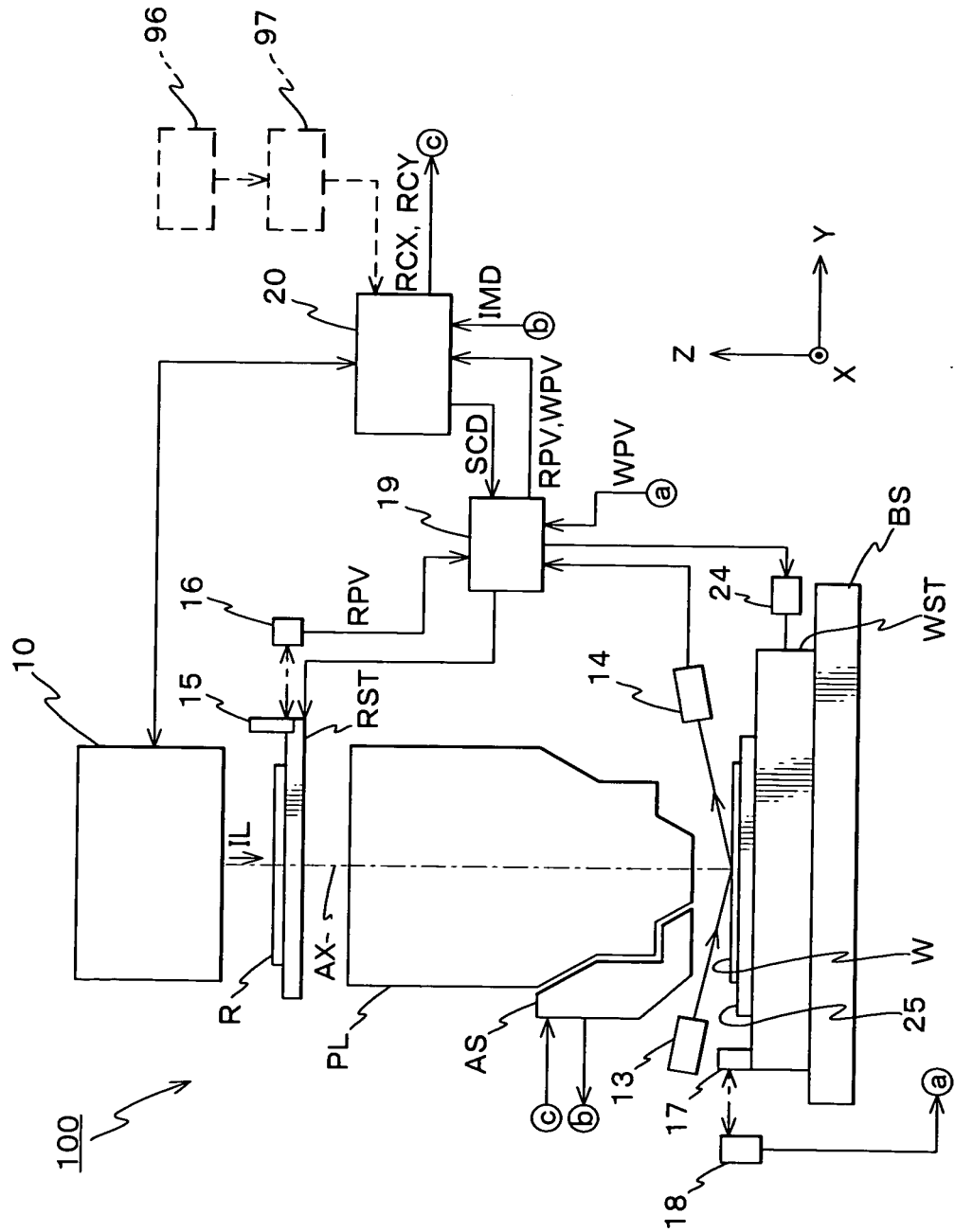


FIG. 1

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Fig. 2A

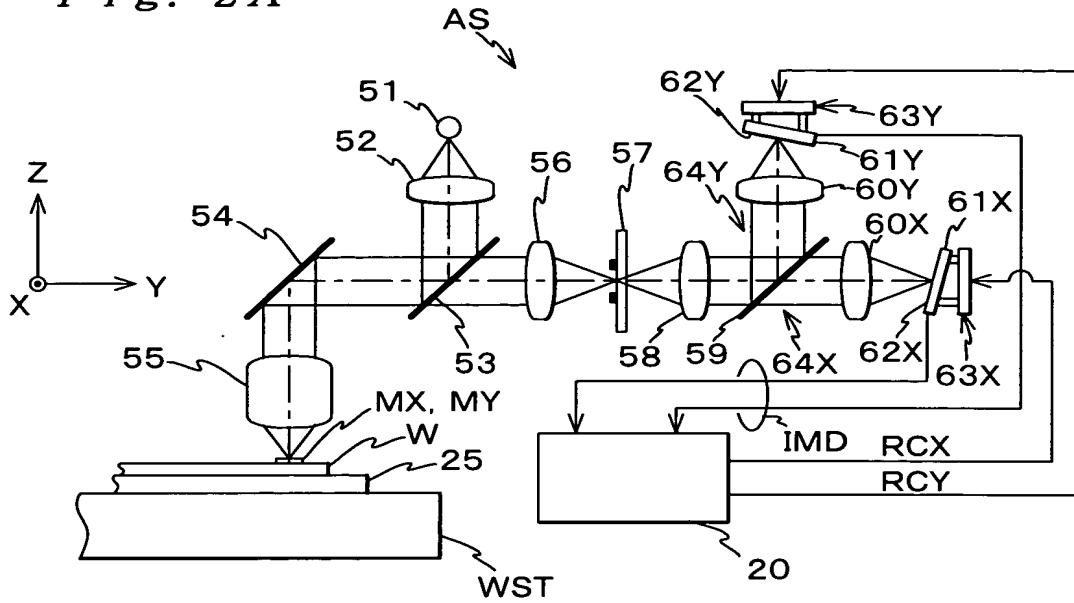


Fig. 2B

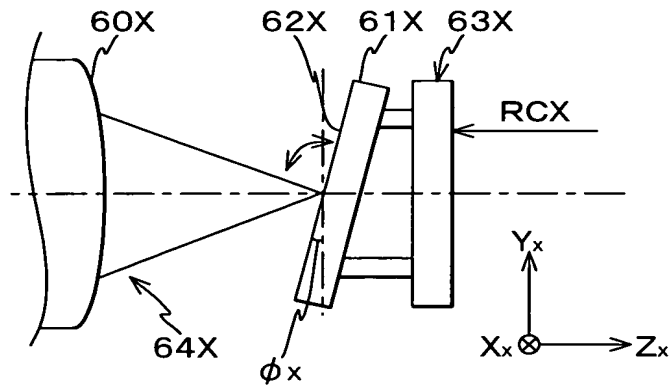
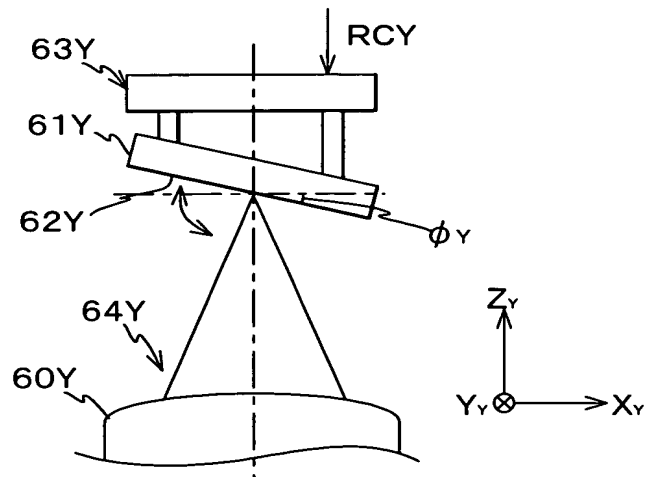


Fig. 2C



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Fig. 3A

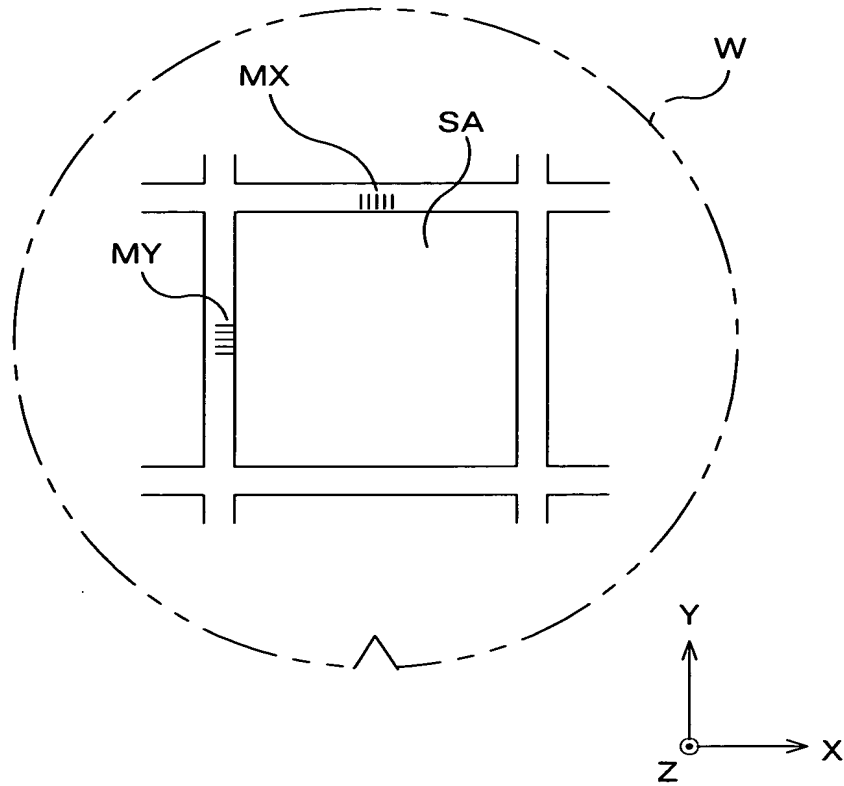
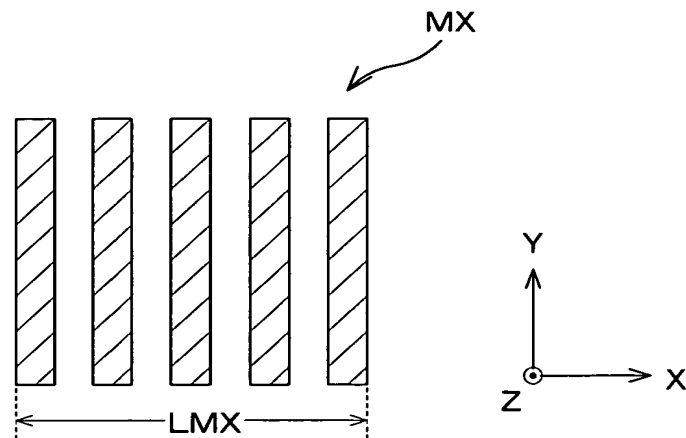


Fig. 3B



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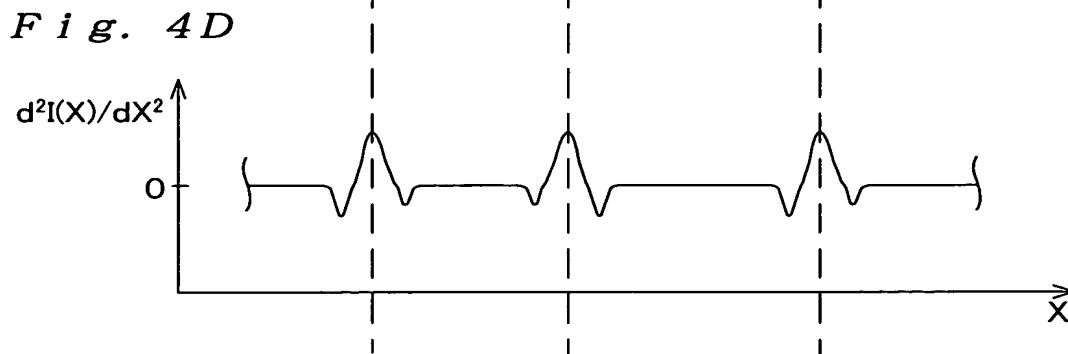
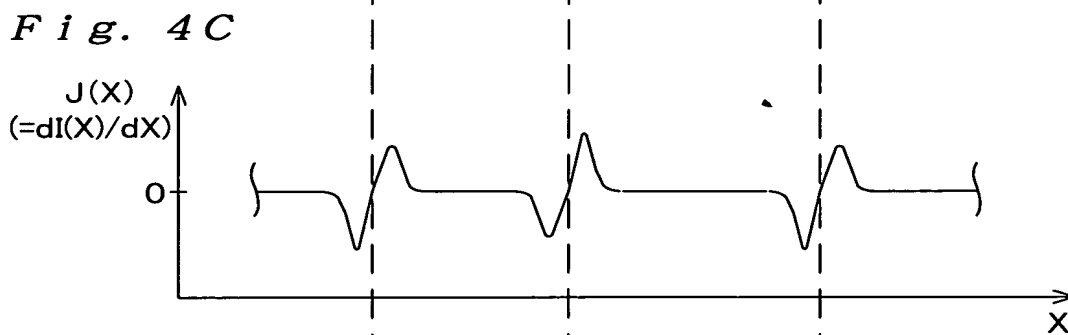
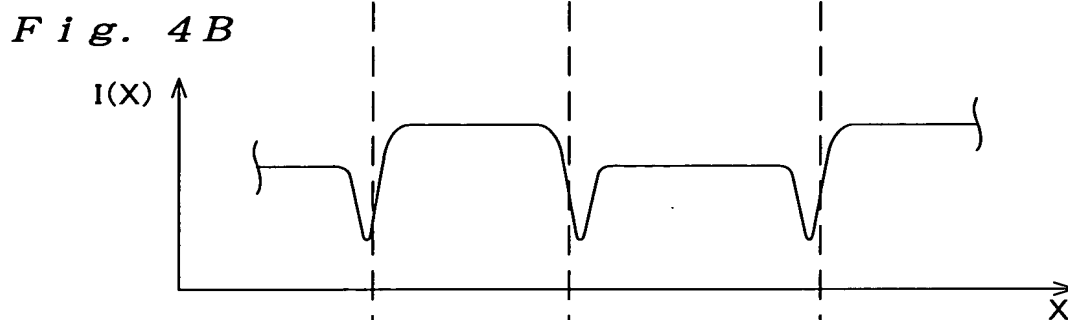
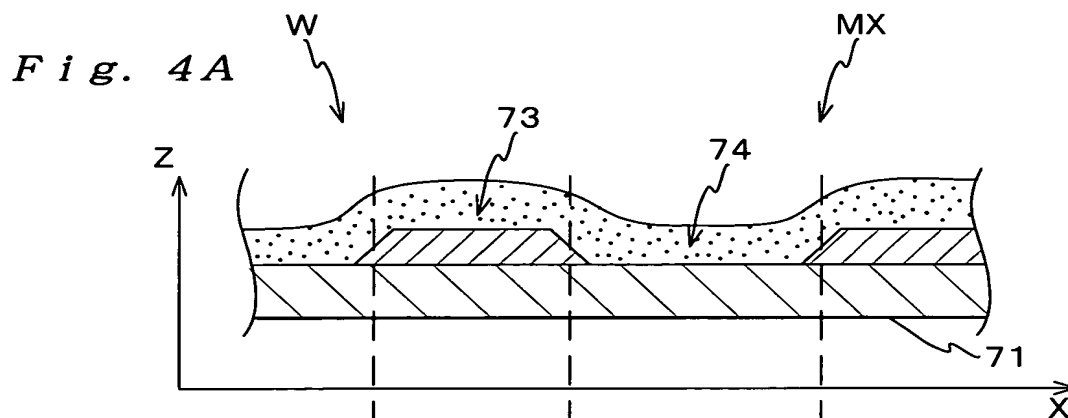


FIG. 4A-4D

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Fig. 5A

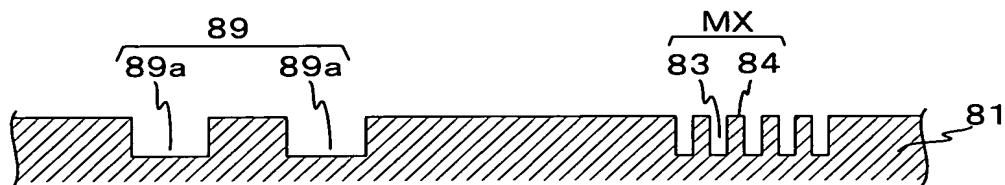


Fig. 5B

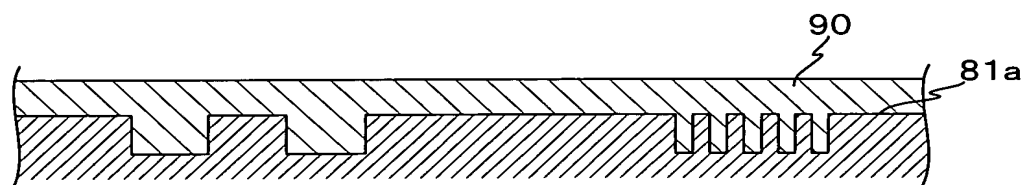


Fig. 5C

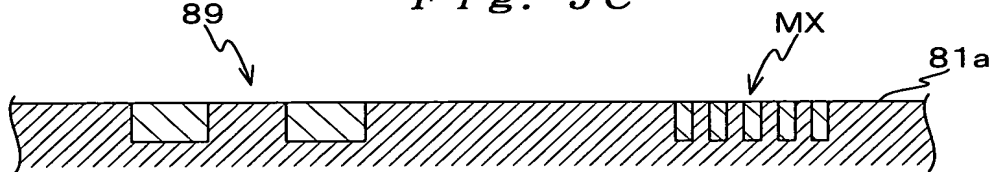


Fig. 5D

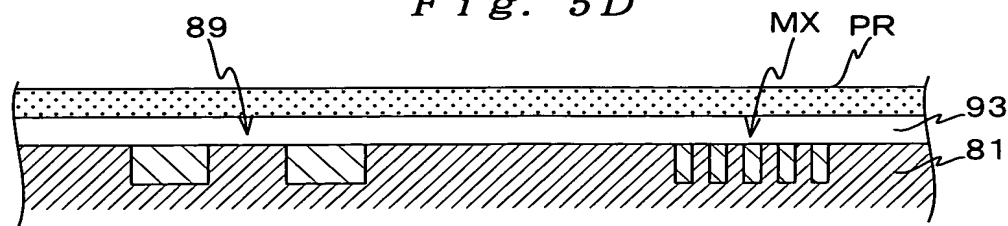
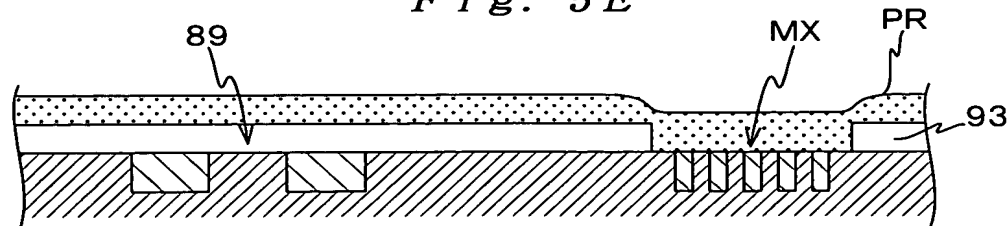


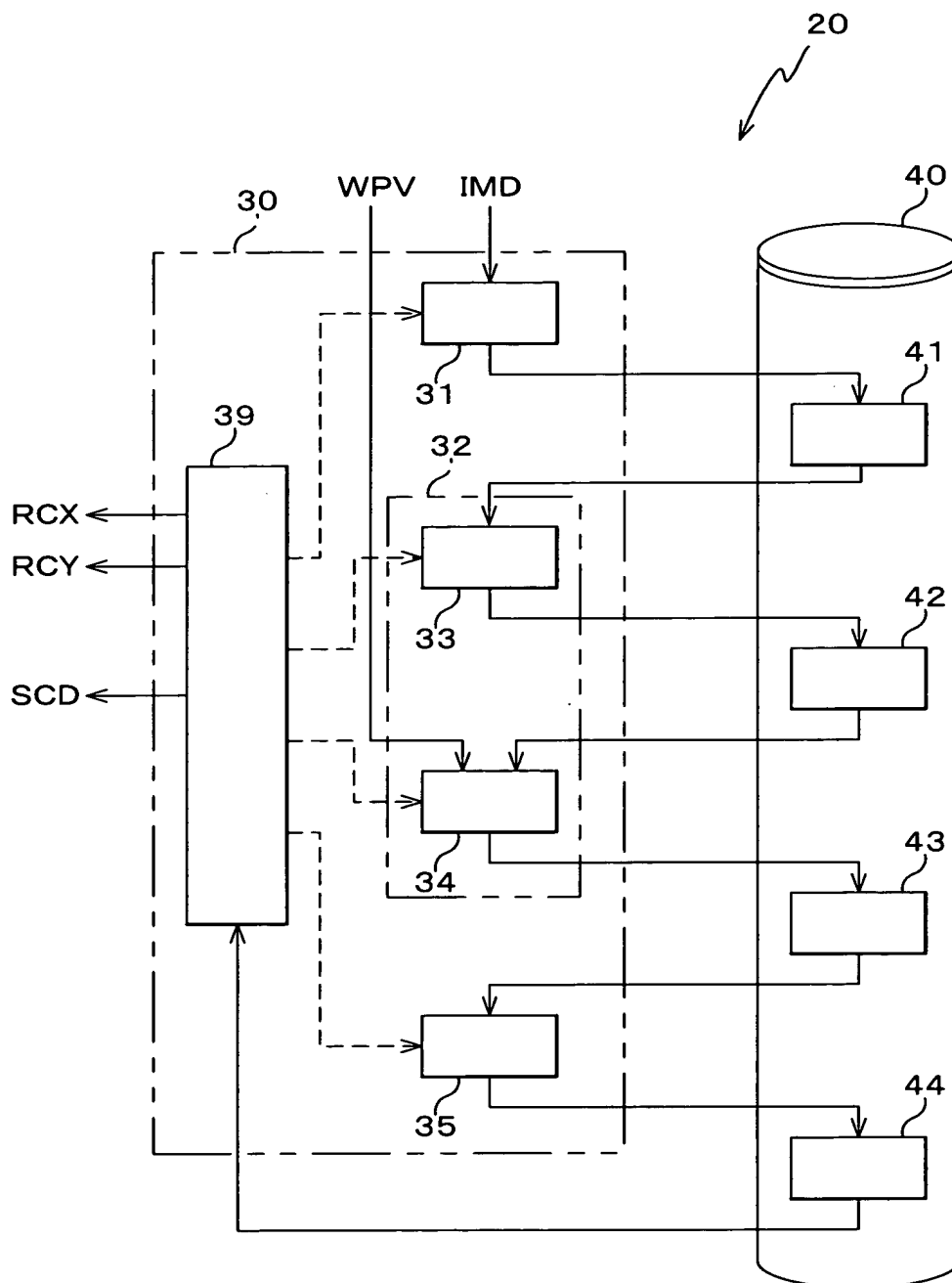
Fig. 5E



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Fig. 6



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Fig. 7

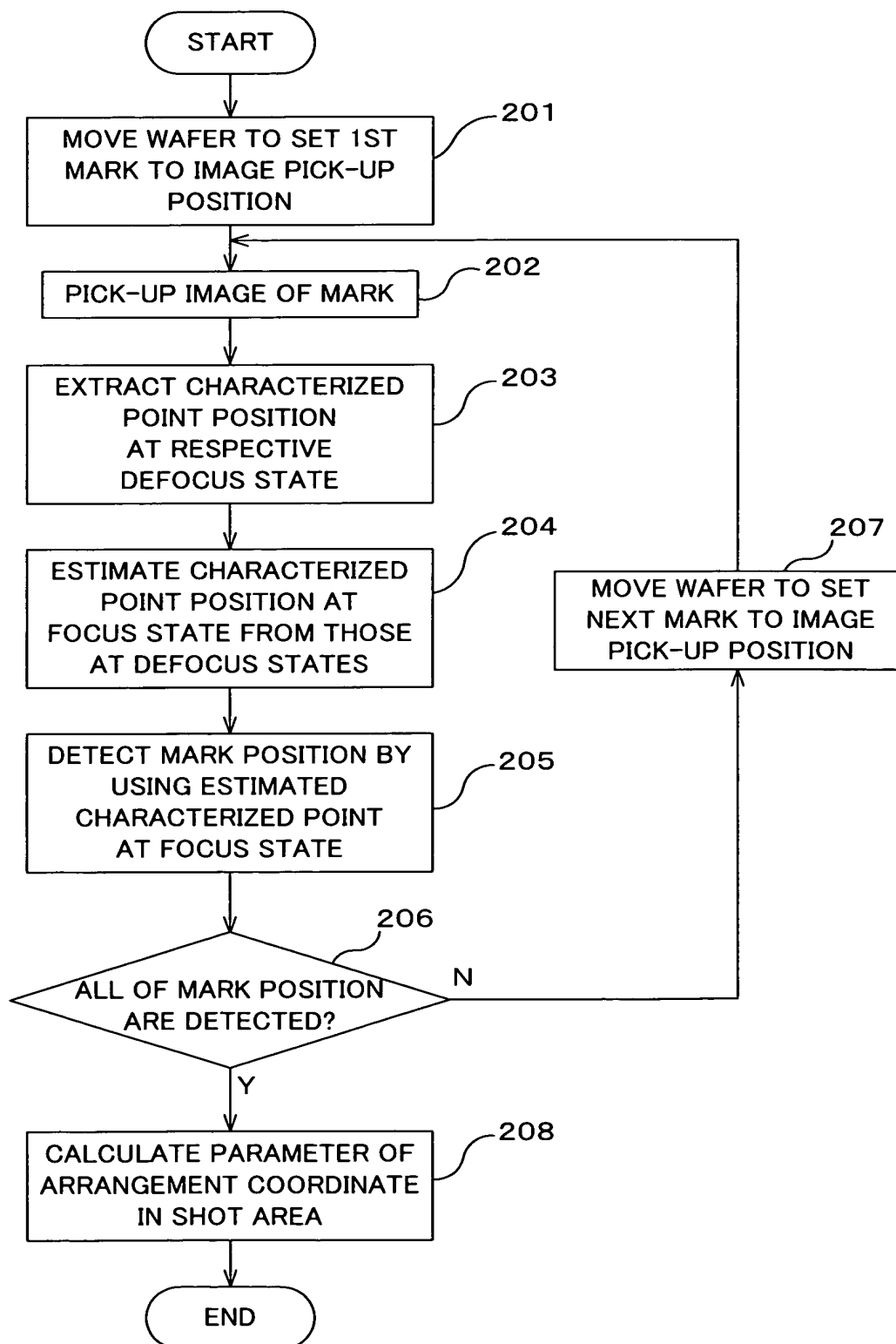
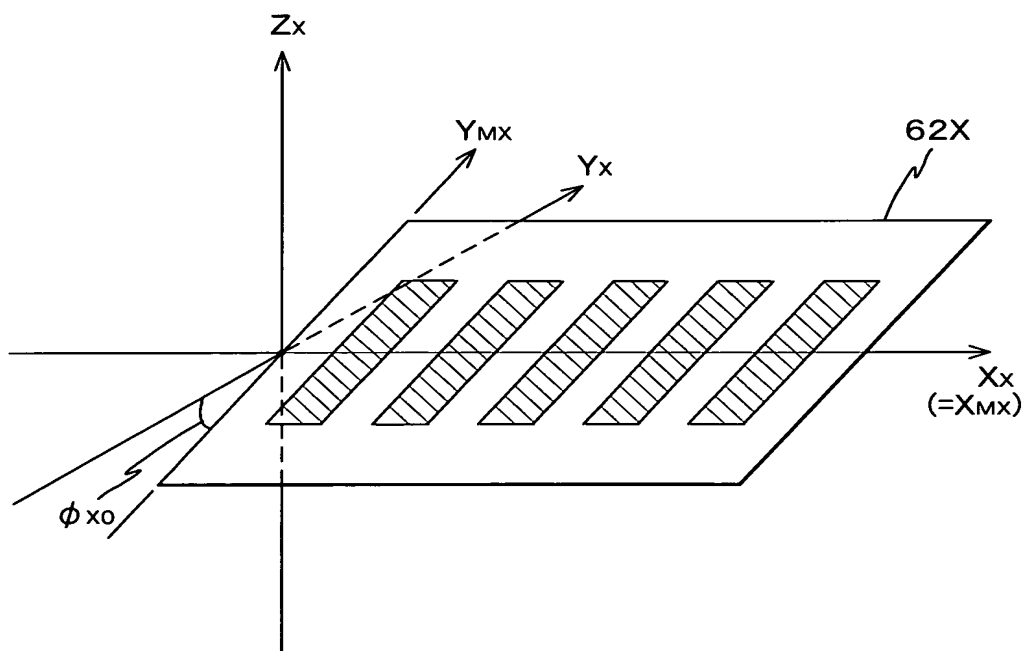


FIG. 7

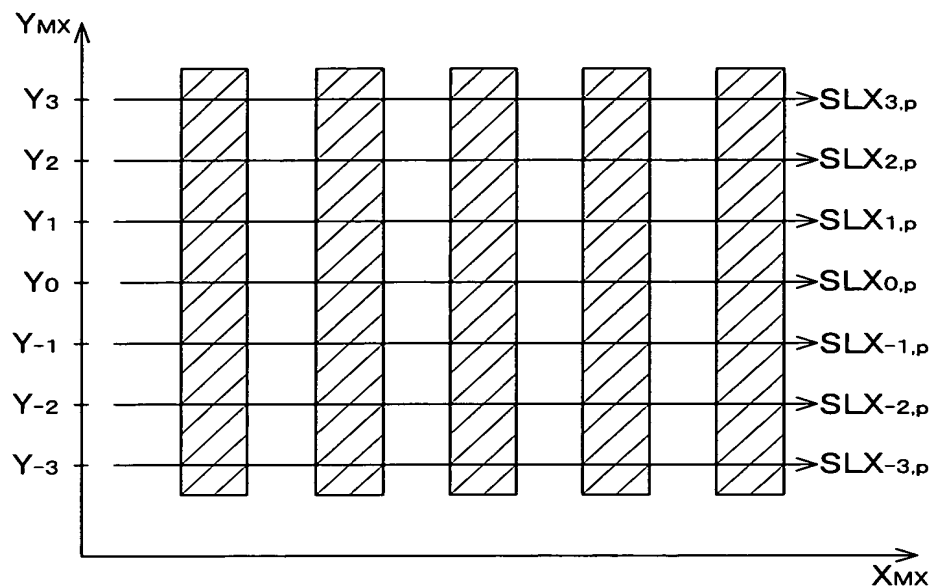
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F i g. 8

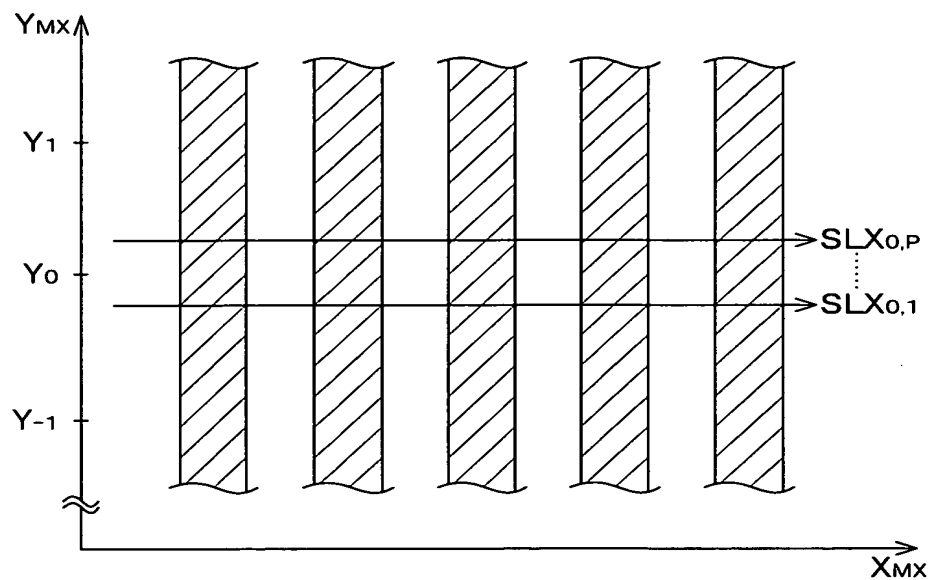


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F i g. 9 A



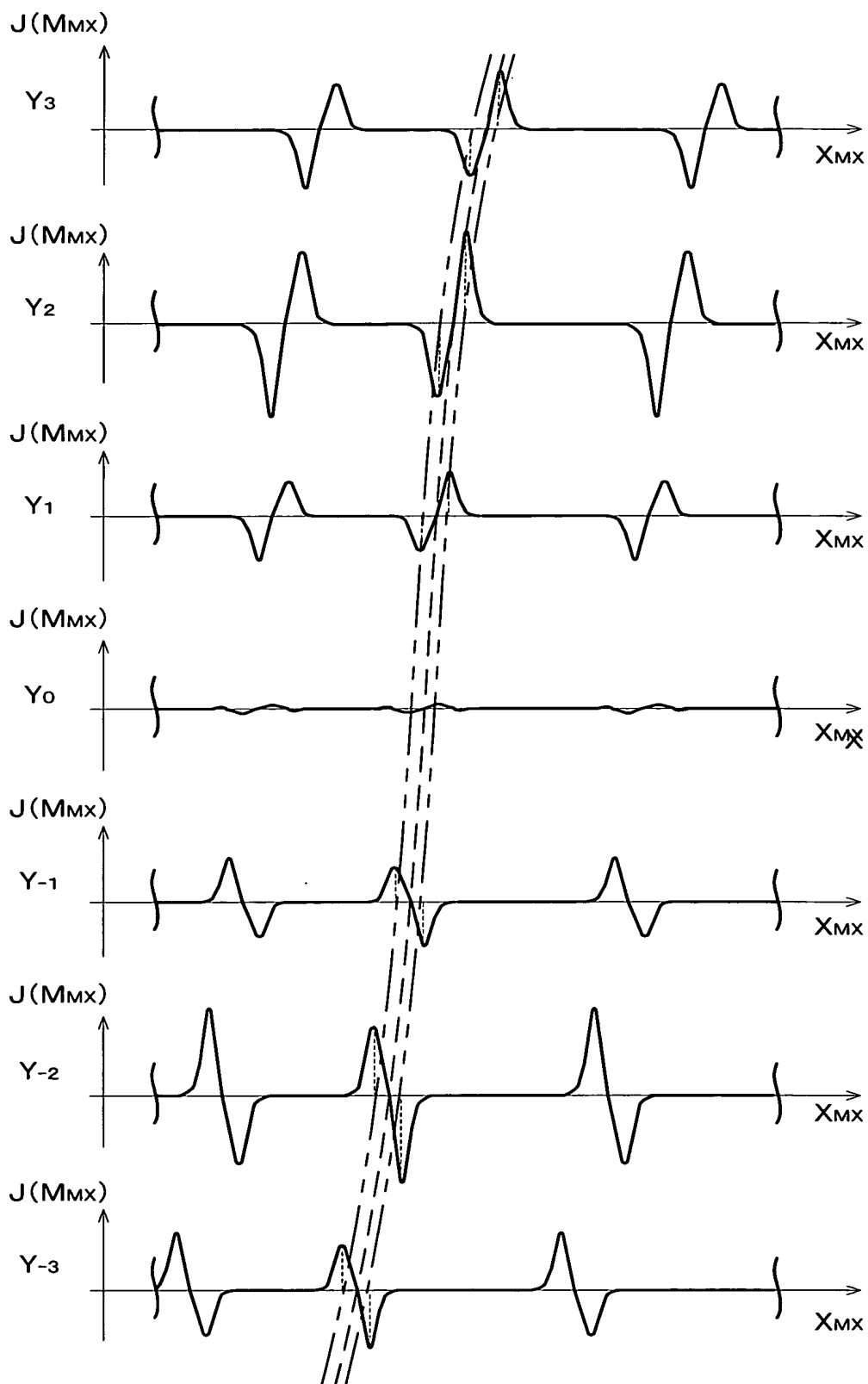
F i g. 9 B



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Fig. 10



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Fig. 11

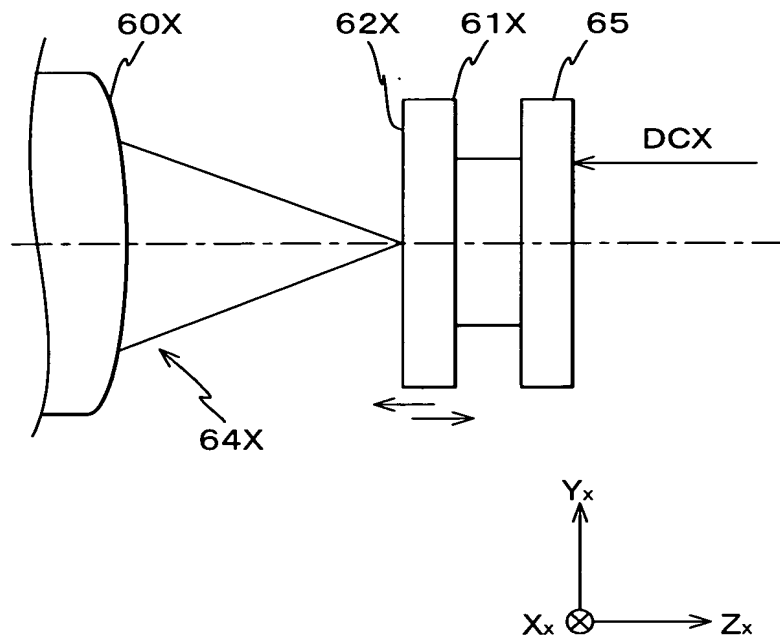


FIG. 11

```
graph TD; 301[DESIGN<br/>(FUNCTION, PERFORMANCE,<br/>PATTERN)] --> 302[MANUFACTURE MASK]; 302 --> 304[PROCESS WAFER]; 303[MANUFACTURE WAFER] --> 304; 304 --> 305[ASSEMBLE DEVICE]; 305 --> 306[INSPECT DEVICE]; 306 --> SHIPMENT["(SHIPMENT)"];
```

The flowchart illustrates a semiconductor manufacturing process. It begins with a box labeled 301 containing the text "DESIGN (FUNCTION, PERFORMANCE, PATTERN)". An arrow points down from 301 to box 302, "MANUFACTURE MASK". From 302, an arrow points down to box 304, "PROCESS WAFER". To the right of 302 is box 303, "MANUFACTURE WAFER", with an arrow pointing down to box 304. From 304, an arrow points down to box 305, "ASSEMBLE DEVICE". From 305, an arrow points down to box 306, "INSPECT DEVICE". Finally, an arrow points down from 306 to the text "(SHIPMENT)".

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Fig. 13

